film comprising silicon and molybdenum, [or] <u>and</u> a multilayer film comprising silicon and tungsten.

157. A device according to claim 152 wherein said portion is located adjacent to a boundary between the source and the channel regions or a boundary between the drain and the channel regions.

## **REMARKS**

The Examiner's Official Action dated February 16, 2000 has been received and its contents carefully noted. Claims 178-157 were pending in the present application prior to the above amendment. Claims 78-88, 90-94, 96-100, 102-116, 118-132, 134-138, 140-144, 146-150 and 152-156 have been amended in order to more specifically recite one feature of the present invention.

Paragraph 2 of the Official Action rejects claims 82, 88, 94, 100, 108, 116, 124 and 132 as indefinite. In response, these claims have been amended and reconsideration is requested in view thereof.

Paragraphs 4-6 of the Official Action rejects claims 78-157 under 35 U.S.C. 103(a) as being unpatentable over Wilson et al. in view of Saito et al., and further Solheim or Higashi. However, Wilson, Saito and Solheim are not related to a display device and Higashi discloses a solid-state imaging device but does not teach an active matrix display device. Since one of the advantageous results of the present invention is a decrease of the leakage current, as described in the specification on page 7, lines 1-10, it is advantageous that the present invention be used in a pixel region of an active matrix type display device. Moreover, since another advantageous result of the present invention is an elevation of the drain voltage resistance, a reliability of the display device can be improved. Also, since impurities such as carbon, nitrogen and oxygen combine with neutralized dangling bonds present within

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carrier generating regions and thereby the density of recombination centers is decreased, the characteristics, e.g. mobility, of the device can be improved (see page 7, lines 25-30).

It is advantageous that the present invention is used in a peripheral circuit of an active matrix type display device, since high operation speed is required for the peripheral circuit. For the above reasons, it is respectfully submitted that the amended claims are patentable.

For all of the above reasons, it is respectively asserted that claims 78-157 are now in proper condition for allowance and reconsideration of the pending rejections is respectively requested. If the Examiner believes that any further discussions would be beneficial in this case, he is invited to contact the undersigned.

Respectfully submitted,

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